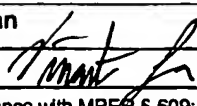


F rm PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. AUS920030549US1		SERIAL NO. Not Assigned	
			APPLICANT DeWitt, Jr. et al.			
			FILING DATE		GROUP ART UNIT Not Assigned	
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE	
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✓	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
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DATE CONSIDERED 4-21-06			EXAMINER 			
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			APPLICANT DeWitt, Jr. et al.			
			FILING DATE		GROUP ART UNIT Not Assigned	

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W	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
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W	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
W	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
W	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001

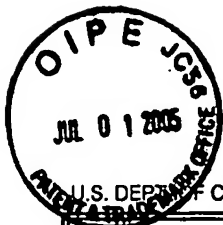
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X

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	BJ Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.
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W	BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

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EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
W	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
W	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

DATE CONSIDERED 4-21-06	EXAMINER <i>[Signature]</i>
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Page 1 of 1

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**LIST OF PRIOR ART CITED
BY APPLICANT**

(Use several sheets if necessary)

ATTORNEY DOCKET NO.
AUS920030549US1SERIAL NO.
10/757,269APPLICANT **DeWitt, Jr. et al.**FILING DATE **January 14, 2004**GROUP ART UNIT **2122****RELATED PATENT APPLICATIONS:**

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
W	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
W	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
W	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
W	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
W	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
W	AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
W	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
W	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
W	AI	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
W	AJ	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
W	AK	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
W	AL	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
W	AM	10/757,248 / AUS920030544US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses to Identify Hot Spots	Jan. 14, 2004
W	AN	10/757,212 / AUS920030553US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Based on Arithmetic Combinations of Performance Counter Values	Jan. 14, 2004
W	AO	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004

DATE CONSIDERED **4-21-06**EXAMINER **Wm J. v**

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Complete if Known

Application Number	10/757,269
Filing Date	01/14/2004
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Unknown
Art Unit	2122
Attorney Docket No.	AUS920030549US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
W	AA 5,797,019	08-18-1998	Levine et al.	
W	AB 6,549,998 B1	04-15-2003	Pekarich et al.	
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W	AE 6,681,387 B1	01-20-2004	Hwu et al.	
W	AF 6,925,424 B2	08-02-2005	Jones et al.	

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NONPATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
W	AG Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
W	AH "Hardware Cycle Based memory Residency", IBM, May 22, 2003, Ip.com, IPCOM000012728D, pp. 1-2
W	AI HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner:

Date Considered:

5-1-06

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